

Fig. 2

103 LEVEL DETECTION UNIT

104 LEVEL DETERMINATION UNIT

105 FILTER COEFFICIENT STORAGE MEMORY

106 VARIABLE FILTER

107 QUANTIZER

FIG. 3

- 1     DIGITAL INTERFACE
- 2     OVER-SAMPLING CIRCUIT
- 3     DIGITAL VOLUME
- 4     DELTA-SIGMA MODULATOR
- 5     PCM/PWM CONVERTER
- 6     POWER SWITCH
- 7     LOW-PASS FILTER

Fig. 6

- 9 QUANTIZATION BIT RATE DETECTION UNIT
- 10 VOLUME SETTING VALUE DETECTION UNIT